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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/601,467	06/23/2003	Brian A. Wims	GP-302957 (2750/30)	7224
7590	10/25/2005			
General Motors Corporation Legal Staff, Mail Code 482-C23-B21 300 Renaissance Center P.O. Box 300 Detroit, MI 48265-4714			EXAMINER NGUYEN, TUNG X	
			ART UNIT 2829	PAPER NUMBER

DATE MAILED: 10/25/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>		Application No. 10/601,467	Applicant(s) WIMS ET AL.
Examiner Tung X. Nguyen	Art Unit 2829		

*-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --  
Period for Reply*

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

1) Responsive to communication(s) filed on 25 April 2005.  
 2a) This action is FINAL.      2b) This action is non-final.  
 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

4) Claim(s) 1-12 is/are pending in the application.  
 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.  
 5) Claim(s) \_\_\_\_\_ is/are allowed.  
 6) Claim(s) 1-12 is/are rejected.  
 7) Claim(s) \_\_\_\_\_ is/are objected to.  
 8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

9) The specification is objected to by the Examiner.  
 10) The drawing(s) filed on \_\_\_\_\_ is/are: a) accepted or b) objected to by the Examiner.  
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).  
 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### Priority under 35 U.S.C. § 119

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
 a) All    b) Some \* c) None of:  
 1. Certified copies of the priority documents have been received.  
 2. Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

#### Attachment(s)

1) Notice of References Cited (PTO-892)  
 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)  
 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
 Paper No(s)/Mail Date \_\_\_\_\_.

4) Interview Summary (PTO-413)  
 Paper No(s)/Mail Date. \_\_\_\_\_.  
 5) Notice of Informal Patent Application (PTO-152)  
 6) Other: \_\_\_\_\_.

**DETAILED ACTION**

***Response to Arguments***

1. Applicant's arguments see Remark on pages 4-6, filed 4/25/05, with respect to claims 1-12 have been fully considered and are persuasive. The final rejection has been withdrawn.

***Claim Rejections - 35 USC § 102***

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

3. Claims 1-12 are rejected under 35 U.S.C. 102(b) as being anticipated by Cook et al. (u.s.p 4,771,234).

As to claim 1, Cook et al discloses in Figs.1-2, a system for testing electronic module comprising: at least one mapping board box (500 of figure 2) including at least one harness port (111 with a 114 of figure 2); at least one harness (114 of figure 2) operably connected at one end to the at least one harness port (fig. 2); the mapping board box (500) including a plurality of pin receptors (via receiving 110 of figure 2) in electronic communication with the at least one harness port (114, 111 of figure 2), the pin receptors adapted to communicate with a circuit printed on a circuit board with pins (105 of figure 2), wherein the mapping board box is pre-wired to receive circuit boards with a variety of pin configurations (fig. 2).

As to claim 2, Cook et al. disclose in Fig. 1, the dimensions of the at least one mapping board box (501, 500 of figure 1) are sized to fit a printed circuit board (105 of figure 1).

As to claims 3-4, 6, Cook et al disclose all of the limitations except for the at least one harness port comprises 56 pins, and/or the at least one mapping board box comprises 560 pin receptors. It is a well known that the harness port comprising 56 pins and the mapping board box having 560 pin receptors for transmitting and receiving all of kinds data at the same time. However, it has been held that discovering an optimum value of a result effective variable involves only routine skill in the art. *In re Boesch*, 617 F.2d 272, 205 USPQ 215 (CCPA 1980).

As to claim 5, Cook et al disclose in Figs. 1-2, the at least one harness comprises a generic harness (114 of figure 2).

As to claim 7, APA discloses the system further comprising an electronic simulator (140 of figure 1).

As to claim 8, Cook et al disclose in Fig. 1, the method comprising steps of: a circuit board (105 of figure 1) corresponding to an electronic module (element on 105 of figure 1) via pins (110 of figure 1); and the circuit board (105) inserting into a pre-wired mapping board box (501, 500 of figure 1).

As to claim 9, Cook et al. disclose in Fig. 1, the steps of receiving the pinned portion (110 of figure 1) of the board (501) into connection port (pins of elements of 105) in a mapping board box (501, 500 of figure 1)

At to claim 10, Cook et al disclose the steps of a circuit (105 of figure 1) and the module (elements on 105) communicates via the mapping board box (501, 500 of figure 1) and a harness connection (114 of figure 2).

As to claim 11, Cook et al disclose in Fig. 1, the system for test an electronic module comprising: means (501 of figure 1) for receiving a circuit board (105 of figure 1); means (106 of figure 1) for stacking the circuit board (105 of figure 1), receiving means (501) and an electronic module (elements on 105) to communicate between the circuit board (105 of figure 1) receiving means (501) and an electronic module (elements on 105).

As to claim 12, Cook et al disclose in Figs. 1-2, the mapping board box (500 of figure 2) includes a plurality of pin receptors (via to receive 110 of figure 2) in electronic communication with the at least one harness port (114, 111 of figure 2), the pin receptors adapted to communicate with a circuit printed on a circuit board with pins (105 of figure 2).

*It is noted that the term "mapping board box" is a broad term, therefore the "fixture base" (500 of figures 1-2) is qualified as "mapping board box".*

#### ***Conclusion***

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tung X. Nguyen whose telephone number is (571) 272-1967. The examiner can normally be reached on 8:30am-5:00pm M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nestor Ramirez can be reached on (571) 272-2034. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

TN  
10/20/05

  
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